

## General Description

The **BCH3-D** is a System-On-Chip (iSOC) solution of High-Efficiency Triple Error Correcting (HE-TEC) encoders/decoders interfacing on one side to a multi megabit/gigabit Sonet/SDH/ATM multiplexer (or any Mux-Demux) and to a High-Efficiency Quadrature Modem or Optical Networks on the other. The BCH3-D is structured for  $(2^8 - 1)$  block lengths, with proprietary techniques for easily handling shortened codes. The BCH3-D is fully synchronous and is easily integrated into your system architecture.

## Applications



The **BCH3-D** finds applications in:

- Radio/Wireless high-capacity Transport and Distribution systems
- Synchronous Optical Networks

## Features

The **BCH3-D**'s main features are:

- High-Efficiency Triple Error Correction (HE-TEC)
- Extensive transmission impairment monitoring
- Applicable to any quadrature modulation
- Applicable to Synchronous Optical Networks

For more information regarding this product, please contact sales at: 1-514-624-4458 or at [sales2@newcoretech.com](mailto:sales2@newcoretech.com)

You may also visit NewCore's web site at: [www.newcoretech.com](http://www.newcoretech.com)